

What is claimed is:

1. Communication circuit arrangement with
bidirectional signal paths, comprising:

a first signal path to transmit a first signal into
5 a first direction, having an input terminal and an
output terminal and including a first digital circuit
block to process said first signal,

a second signal path to transmit a second signal
into a second direction having an input terminal and an
10 output terminal and including a second digital circuit
block to process said second signal and

a first switch having a first terminal coupled to a
first circuit node within the first signal path and a
second terminal coupled to a second circuit node within
15 the second signal path to provide a test signal loop
during a test mode of said circuit arrangement.

2. Communication circuit arrangement according to
claim 1, wherein said first signal path comprises a
20 first analog circuit block coupled between said first
digital circuit block and said output terminal of said
first signal path and wherein said second signal path
comprises a second analog circuit block coupled between

said second digital circuit block and said input terminal of said second signal path.

3. Communication circuit arrangement according to
5 claim 1, wherein said first digital circuit block is a baseband processing block and said first signal path comprises a first analog circuit block to convert said first signal into a radio frequency signal, said first analog circuit block coupled between said first digital
10 circuit block and said output terminal of said first signal path, and wherein said second digital circuit block is a baseband processing block and said second signal path comprises a second analog circuit block to convert a received radio signal into said second signal,
15 said second analog circuit block coupled between said input terminal of said second signal path and said second digital circuit block.

4. Communication circuit arrangement according to
20 claim 1, wherein a second switch is provided having a first terminal connected to said first terminal of said first switch and a second terminal connected to said output terminal of said first signal path and wherein a third switch is provided having a first terminal

connected to said input terminal of said second signal path and a second terminal connected to said second terminal of said first switch.

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